

Quassel IRC - Bug #1521

Quassel requests 1ms timer precision on Windows client

03/17/2019 10:11 PM - warpsharp

Status:	New	Start date:	03/17/2019
Priority:	Normal	Due date:	
Assignee:		% Done:	0%
Category:		Estimated time:	0.00 hour
Target version:		OS:	Windows
Version:	0.13.0		
Description Quassel is requesting 1ms timer precision on the Windows client which has the potential to hurt battery life on mobile devices. Is there any particular reason that quassel is requesting such high precision? Or is it just a QT thing that's getting bundled? This should be a relatively straightforward fix if it doesn't need that precision. Just not sure where it's coming from yet but I can try and look into it.			

History

#1 - 11/12/2021 11:40 AM - ben-zen

From some code investigation, Quassel's using a bunch of QTimer's, and I went and dug into how Qt itself handles timers; the really juicy details are in https://code.qt.io/cgit/qt/qtbase.git/tree/src/corelib/kernel/qeventdispatcher_win.cpp -- searching for SetTimer finds the way these timers are created.

Basically, Qt is using the non-coalesceable timers for everything; this seems like a spot where it'd be advantageous to make possibly both CoarseTimer and VeryCoarseTimer timers not just round their intervals, but also switch to coalescing overall. It looked like basically all the timers in Quassel are coarse, so this would immediately benefit this codebase. But this *is* an attempt to patch something in the framework, so who knows when it'd happen.

Within Quassel, it's also worth seeing if all these timers are actually needed. I haven't done a full survey yet to see if there's really no other way to achieve some goals.

#2 - 04/04/2025 09:20 PM - user250404

I just ran into this on Quassel 0.13.1. AMD Ryzen processors cannot enter deep idle states (C-states) at the package level if a very fast system timer resolution (<5ms) is set, continually wasting around 5W of power.

#3 - 04/04/2025 09:31 PM - user250404

0.14.0 is affected too.